JAI 1 5 2002 & S/N 09/259,849

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Paul A. Farrar

Examiner: Ginette Peralta

**PATENT** 

Serial No.:

09/259,849

Group Art Unit: 2814

Filed:

March 1, 1999

Docket: 303.557US1

Title:

CONDUCTIVE STRUCTURES IN INTEGRATED CIRCUITS

## SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents Washington, D.C. 20231

In compliance with the duty imposed by 37 C.F.R. § 1.56, and in accordance with C.F.R. §§ 1.97 et. seq., the enclosed materials are brought to the attention of the Examiner for consideration in connection with the above-identified patent application. Applicant respectfully requests that this Information Disclosure Statement be entered and the documents listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to the provisions of MPEP 609, Applicant further request that a copy of the 1449 form, initialled by the Examiner to indicate that all listed citations have been considered, be returned with the next official communication.

Applicant have included the fee of \$180.00 under 37 C.F.R. §§ 1.97(c) and 1.17(p). Please charge any additional fees or credit any overpayment to Account No. 19-0743.

The Examiner is invited to contact the Applicant's Representative at the below-listed telephone number if there are any questions regarding this communication.

Respectfully submitted,

PAUL A. FARRAR

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By his Representatives,

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Date November 20, 2001

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<u>CERTIFICATE UNDER 37 CFR 1.8:</u> The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner of Patents, Washington, D.C. 20231, on this day of November, 2001.

Gina M Uphus

Signatur

Name